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PATENT  
Customer Number 22,852  
Attorney Docket No. 04329.2204-01000

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
Yuuichi MIKATA et al. ) Group Art Unit: 2813  
Application No.: 10/775,065 ) Examiner: L. Schillinger  
Filed: February 11, 2004 ) Notice of Allowance Dated: April 19, 2005  
For: RELIABLE SEMICONDUCTOR ) Confirmation No.: 1299  
DEVICE AND METHOD OF )  
MANUFACTURING THE SAME )

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**LETTER REQUESTING ACKNOWLEDGMENT OF RECEIPT OF  
PRIORITY DOCUMENT**

Applicants are in receipt of the Notice of Allowance and attached Notice of Allowability dated April 19, 2005.

Applicants have claimed foreign priority in both the present application and in parent application no. 09/459,913, filed December 14, 1999 (now U.S. Patent No. 6,713,824, issued March 30, 2004), based on Japanese Patent Application No. 10-375423, filed December 15, 1998. However, in the Notice of Allowability, the Examiner noted that the certified copy of the Japanese Patent Application had not been received for the above-noted application.

Accordingly, attached is a copy of the Claim for Priority filed March 22, 2000 in parent application no. 09/459,913, including the first page of the Japanese Patent Application, along with the date-stamped postcard receipt as proof that the certified copy of the Japanese Patent Application was filed in the parent application.

The Examiner is respectfully requested to issue a Supplemental Notice of Allowability (PTOL-37) acknowledging receipt of the claim for priority and the certified copy.

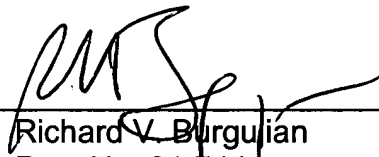
Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

Dated:

5/12/05

By:

  
Richard V. Burgulian  
Reg. No. 31,744

RVB/FPD/acd

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ATTORNEY DOCKET NO. 04329.2204

Assistant Commissioner  
for Patents  
Washington, D.C. 20231

U.S. Patent Application for RELIABLE SEMICONDUCTOR DEVICE AND METHOD OF  
MANUFACTURING THE SAME

Inventors: Yuuichi MIKATA et al.  
Serial No.: 09/459,913  
Filed: December 14, 1999

Group Art Unit: 2811

CLAIM FOR PRIORITY

Sir:


Under the provisions of Section 119 of 35 U.S.C., applicants hereby claim  
the benefit of the filing date of Japanese Patent Application No. 0-375423 filed  
December 15, 1998, for the above identified United States Patent Application.

In support of applicants' claim for priority, filed herewith is one certified copy of  
the above.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW  
GARRETT & DUNNER, L.L.P.

by:

  
Richard V. Burgujian  
Reg. No. 31,744

Dated: 3/21/00



日本国特許庁  
PATENT OFFICE  
JAPANESE GOVERNMENT

別紙添付の書類に記載されている事項は下記の出願書類に記載されて  
いる事項と同一であることを証明する。

This is to certify that the annexed is a true copy of the following application as filed  
with this Office.

出願年月日

Date of Application:

1998年12月15日

出願番号

Application Number:

平成10年特許願第375423号

願人

Applicant(s):

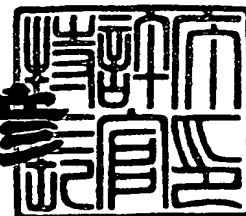
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1999年11月26日

特許庁長官  
Commissioner,  
Patent Office

近藤隆彦



出証番号 出証特平11-308320



RUB- LSH

Application of: Yuuichi MIKATA et al.

Serial No: 09/459,913

Group Art Unit: 2811

Filed: December 14, 1999

For: RELIABLE SEMICONDUCTOR DEVICE AND METHOD OF  
MANUFACTURING THE SAME

**PLEASE STAMP TO ACKNOWLEDGE RECEIPT OF THE FOLLOWING:**

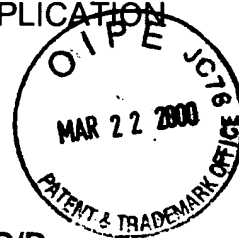
1. CLAIM FOR PRIORITY
2. CERTIFIED COPY OF JAPANESE PATENT APPLICATION  
NO. 10-375423 FILED DECEMBER 15, 1998

Dated: MAR 22 2000

CASE REF: 04329.2204

FPD Domestic Prosecution Team/dlw

MAR 22 2000



N/D/D

DHts  
2-23-00  
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